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17/04




Nimra College of Engineering & Technology


Estd. By Nimra Educational Society (A Muslim Minority Society)
Affiliated to JNTUK, Approved by AICTE, New Delhi, Permitted by Govt. of A.P.
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RESULT ANALYSIS

A.Y:2018-19

S.NO	NAME OF THE PROGRAM	NO.OF STUDENTS REGISTERD	NO.OF STUDENTS PASSED
1	B.Tech-EEE	4	3
2	M.Tech-CAD/CAM	8	8
3	M.Tech-PE	7	7
4	M.Tech-CSE	11	9
5	M.Tech-VLSI-SD	13	13


EXAMINATIONS INCHARGE
NIMRA COLLEGE OF
Engineering & Technology
JUPUDI


PRINCIPAL
NIMRA COLLEGE OF ENGG. & TECH.
NIMRA NAGAR,
IBRAHIMPATNAM-521 456
VIJAYAWADA.

Ay B. Tan 2018-19



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for IV B.Tech II Semester [R13,R10] Regular/Supplementary Examinations April/May-2019
College: NIMRA COLLEGE OF ENGG. JUPUDI, IBRAHIMPATNAM:23

Htno	Subcode	Subname	Internal	External	Credits
16235A0201	RT42021	DIGITAL CONTROL SYSTEMS	27	35	3
16235A0201	RT42022C	SPECIAL ELECTRICAL MACHINES	25	50	3
16235A0201	RT42023C	FACTS: FLEXIBLE ALTERNATING CURRENT TRANSMI	25	40	3
16235A0201	RT42024A	OOPS THROUGH JAVA	26	31	3
16235A0201	RT42025	PROJECT	60	135	9
16235A0202	RT42021	DIGITAL CONTROL SYSTEMS	26	36	3
16235A0202	RT42022C	SPECIAL ELECTRICAL MACHINES	27	58	3
16235A0202	RT42023C	FACTS: FLEXIBLE ALTERNATING CURRENT TRANSMI	26	40	3
16235A0202	RT42024A	OOPS THROUGH JAVA	26	36	3
16235A0202	RT42025	PROJECT	60	135	9
16235A0204	RT42021	DIGITAL CONTROL SYSTEMS	27	11	0
16235A0204	RT42022C	SPECIAL ELECTRICAL MACHINES	24	4	0
16235A0204	RT42023C	FACTS: FLEXIBLE ALTERNATING CURRENT TRANSMI	26	50	3
16235A0204	RT42024A	OOPS THROUGH JAVA	26	24	3
16235A0204	RT42025	PROJECT	58	124	9
16235A0205	RT42021	DIGITAL CONTROL SYSTEMS	26	36	3
16235A0205	RT42022C	SPECIAL ELECTRICAL MACHINES	25	47	3
16235A0205	RT42023C	FACTS: FLEXIBLE ALTERNATING CURRENT TRANSMI	25	45	3
16235A0205	RT42024A	OOPS THROUGH JAVA	25	29	3
16235A0205	RT42025	PROJECT	60	128	9

**NOTE:1 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: 01-06-2019]

** Note:**

- 1 in the filed of externals indicates student absent for the respective subject.
- * -2 in the filed of externals indicates student Withheld for the respective subject.
- * -3 in the filed of externals indicates student Malpractice for the respective subject.

Date:25-05-2019

N. Mohan Rao
Controller of Examinations

A-y m.Tech 2018-19



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
 Results for M.Tech II SEMESTER (R16 / R13) Regular / Supplementary Examinations , JUNE- 2018 .
 College: NIMRA COLLEGE OF ENGG., JUPUDI, IBRAHIMPATNAM:23

Discrepancy pertaining to these results are to be submitted on or before 24-09-2018 with following documents at CE(PG) Office, JNTUK, Kakinada

Htno	Subcode	Subname	Internal	External	credits
17231D0401	J0401	MODELING & SIMULATION OF MANUFACTURING S	36	27	1
17231D0401	J0402	COMPUTER GRAPHICS	37	29	1
17231D0401	J0405	CONCURRENT ENGINEERING ELECTIVEIII	34	32	1
17231D0401	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	34	51	1
17231D0401	J1501	OPTIMIZATION AND RELIABILITY	33	25	1
17231D0401	J2103	FINITE ELEMENT METHODS	34	25	1
17231D0401	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	34	24	1
17231D0402	J0401	MODELING & SIMULATION OF MANUFACTURING S	36	43	1
17231D0402	J0402	COMPUTER GRAPHICS	36	39	1
17231D0402	J0405	CONCURRENT ENGINEERING ELECTIVEIII	38	38	1
17231D0402	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	38	55	1
17231D0402	J1501	OPTIMIZATION AND RELIABILITY	37	29	1
17231D0402	J2103	FINITE ELEMENT METHODS	36	26	1
17231D0402	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	35	24	1
17231D0403	J0401	MODELING & SIMULATION OF MANUFACTURING S	34	43	1
17231D0403	J0402	COMPUTER GRAPHICS	34	33	1
17231D0403	J0405	CONCURRENT ENGINEERING ELECTIVEIII	34	39	1
17231D0403	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	34	52	1
17231D0403	J1501	OPTIMIZATION AND RELIABILITY	37	30	1
17231D0403	J2103	FINITE ELEMENT METHODS	33	24	1
17231D0403	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	31	25	1
17231D0404	J0401	MODELING & SIMULATION OF MANUFACTURING S	35	31	1
17231D0404	J0402	COMPUTER GRAPHICS	34	28	1
17231D0404	J0405	CONCURRENT ENGINEERING ELECTIVEIII	33	36	1
17231D0404	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	34	51	1
17231D0404	J1501	OPTIMIZATION AND RELIABILITY	34	24	1
17231D0404	J2103	FINITE ELEMENT METHODS	37	25	1
17231D0404	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	35	25	1
17231D0405	J0401	MODELING & SIMULATION OF MANUFACTURING S	36	35	1
17231D0405	J0402	COMPUTER GRAPHICS	32	36	1
17231D0405	J0405	CONCURRENT ENGINEERING ELECTIVEIII	34	43	1
17231D0405	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	35	52	1
17231D0405	J1501	OPTIMIZATION AND RELIABILITY	34	31	1
17231D0405	J2103	FINITE ELEMENT METHODS	35	28	1
17231D0405	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	35	24	1
17231D0406	J0401	MODELING & SIMULATION OF MANUFACTURING S	37	30	1
17231D0406	J0402	COMPUTER GRAPHICS	38	37	1
17231D0406	J0405	CONCURRENT ENGINEERING ELECTIVEIII	38	31	1
17231D0406	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	39	58	1
17231D0406	J1501	OPTIMIZATION AND RELIABILITY	38	28	1
17231D0406	J2103	FINITE ELEMENT METHODS	39	24	1
17231D0406	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	39	24	1

Htno	Subcode	Subname	Internal	External	credits
17231D0409	J0401	MODELING & SIMULATION OF MANUFACTURING S	35	41	1
17231D0409	J0402	COMPUTER GRAPHICS	34	39	1
17231D0409	J0405	CONCURRENT ENGINEERING ELECTIVEIII	37	26	1
17231D0409	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	35	53	1
17231D0409	J1501	OPTIMIZATION AND RELIABILITY	33	28	1
17231D0409	J2103	FINITE ELEMENT METHODS	32	27	1
17231D0409	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	33	24	1
17231D0410	J0401	MODELING & SIMULATION OF MANUFACTURING S	36	40	1
17231D0410	J0402	COMPUTER GRAPHICS	35	48	1
17231D0410	J0405	CONCURRENT ENGINEERING ELECTIVEIII	35	40	1
17231D0410	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	35	53	1
17231D0410	J1501	OPTIMIZATION AND RELIABILITY	37	39	1
17231D0410	J2103	FINITE ELEMENT METHODS	36	27	1
17231D0410	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	34	24	1
17231D4301	J4301	SWITCHED MODE POWER CONVERSION	39	28	1
17231D4301	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	36	29	1
17231D4301	J4303	DIGITAL CONTROLLERS	39	27	1
17231D4301	J4304	CUSTOM POWER DEVICES	40	26	1
17231D4301	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	40	24	1
17231D4301	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	35	28	1
17231D4301	J4310	POWER CONVERTERS & DRIVES LABORATORY	38	57	1
17231D4302	J4301	SWITCHED MODE POWER CONVERSION	30	27	1
17231D4302	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	28	26	1
17231D4302	J4303	DIGITAL CONTROLLERS	30	26	1
17231D4302	J4304	CUSTOM POWER DEVICES	29	25	1
17231D4302	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	29	26	1
17231D4302	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	30	30	1
17231D4302	J4310	POWER CONVERTERS & DRIVES LABORATORY	35	55	1
17231D4303	J4301	SWITCHED MODE POWER CONVERSION	37	35	1
17231D4303	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	36	39	1
17231D4303	J4303	DIGITAL CONTROLLERS	36	24	1
17231D4303	J4304	CUSTOM POWER DEVICES	35	35	1
17231D4303	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	36	33	1
17231D4303	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	35	36	1
17231D4303	J4310	POWER CONVERTERS & DRIVES LABORATORY	38	56	1
17231D4304	J4301	SWITCHED MODE POWER CONVERSION	39	26	1
17231D4304	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	34	26	1
17231D4304	J4303	DIGITAL CONTROLLERS	36	24	1
17231D4304	J4304	CUSTOM POWER DEVICES	36	28	1
17231D4304	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	37	26	1
17231D4304	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	35	32	1
17231D4304	J4310	POWER CONVERTERS & DRIVES LABORATORY	37	56	1
17231D4305	J4301	SWITCHED MODE POWER CONVERSION	33	25	1
17231D4305	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	32	32	1
17231D4305	J4303	DIGITAL CONTROLLERS	35	24	1
17231D4305	J4304	CUSTOM POWER DEVICES	31	25	1
17231D4305	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	33	39	1
17231D4305	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	33	37	1
17231D4305	J4310	POWER CONVERTERS & DRIVES LABORATORY	38	56	1
17231D4306	J4301	SWITCHED MODE POWER CONVERSION	36	24	1
17231D4306	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	32	24	1

Htno	Subcode	Subname	Internal	External	credits
17231D4306	J4303	DIGITAL CONTROLLERS	36	24	1
17231D4306	J4304	CUSTOM POWER DEVICES	33	25	1
17231D4306	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	35	26	1
17231D4306	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	36	25	1
17231D4306	J4310	POWER CONVERTERS & DRIVES LABORATORY	37	55	1
17231D4307	J4301	SWITCHED MODE POWER CONVERSION	38	40	1
17231D4307	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	37	32	1
17231D4307	J4303	DIGITAL CONTROLLERS	37	26	1
17231D4307	J4304	CUSTOM POWER DEVICES	38	43	1
17231D4307	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	39	34	1
17231D4307	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	38	45	1
17231D4307	J4310	POWER CONVERTERS & DRIVES LABORATORY	37	58	1
17231D5801	J0502	SOFTWARE ENGINEERING ELECTIVE I	36	26	1
17231D5801	J2503	CYBER SECURITY	38	26	1
17231D5801	J2510	CLOUD COMPUTING ELECTIVE II	39	26	1
17231D5801	J4001	ADVANCED UNIX PROGRAMMING	40	25	1
17231D5801	J4002	BIG DATA ANALYTICS	38	31	1
17231D5801	J5801	COMPUTER NETWORKS	39	24	1
17231D5801	J5803	CSE LAB 2	38	57	1
17231D5802	J0502	SOFTWARE ENGINEERING ELECTIVE I	27	26	1
17231D5802	J2503	CYBER SECURITY	33	27	1
17231D5802	J2510	CLOUD COMPUTING ELECTIVE II	28	28	1
17231D5802	J4001	ADVANCED UNIX PROGRAMMING	30	27	1
17231D5802	J4002	BIG DATA ANALYTICS	33	24	1
17231D5802	J5801	COMPUTER NETWORKS	33	24	1
17231D5802	J5803	CSE LAB 2	37	42	1
17231D5803	J0502	SOFTWARE ENGINEERING ELECTIVE I	11	-1	0
17231D5803	J2503	CYBER SECURITY	19	-1	0
17231D5803	J2510	CLOUD COMPUTING ELECTIVE II	15	-1	0
17231D5803	J4001	ADVANCED UNIX PROGRAMMING	14	-1	0
17231D5803	J4002	BIG DATA ANALYTICS	16	-1	0
17231D5803	J5801	COMPUTER NETWORKS	17	-1	0
17231D5803	J5803	CSE LAB 2	35	40	1
17231D5804	J0502	SOFTWARE ENGINEERING ELECTIVE I	32	39	1
17231D5804	J2503	CYBER SECURITY	36	26	1
17231D5804	J2510	CLOUD COMPUTING ELECTIVE II	36	25	1
17231D5804	J4001	ADVANCED UNIX PROGRAMMING	39	24	1
17231D5804	J4002	BIG DATA ANALYTICS	37	30	1
17231D5804	J5801	COMPUTER NETWORKS	38	24	1
17231D5804	J5803	CSE LAB 2	37	57	1
17231D5805	J0502	SOFTWARE ENGINEERING ELECTIVE I	33	25	1
17231D5805	J2503	CYBER SECURITY	38	25	1
17231D5805	J2510	CLOUD COMPUTING ELECTIVE II	38	28	1
17231D5805	J4001	ADVANCED UNIX PROGRAMMING	38	26	1
17231D5805	J4002	BIG DATA ANALYTICS	37	24	1
17231D5805	J5801	COMPUTER NETWORKS	38	28	1
17231D5805	J5803	CSE LAB 2	37	56	1
17231D5806	J0502	SOFTWARE ENGINEERING ELECTIVE I	26	-1	0
17231D5806	J2503	CYBER SECURITY	31	-1	0
17231D5806	J2510	CLOUD COMPUTING ELECTIVE II	27	-1	0
17231D5806	J4001	ADVANCED UNIX PROGRAMMING	28	-1	0

Htno	Subcode	Subname	Internal	External	credits
17231D5806	J4002	BIG DATA ANALYTICS	29	13	0
17231D5806	J5801	COMPUTER NETWORKS	26	-1	0
17231D5806	J5803	CSE LAB 2	35	50	1
17231D5807	J0502	SOFTWARE ENGINEERING ELECTIVE I	31	24	1
17231D5807	J2503	CYBER SECURITY	36	29	1
17231D5807	J2510	CLOUD COMPUTING ELECTIVE II	36	36	1
17231D5807	J4001	ADVANCED UNIX PROGRAMMING	37	30	1
17231D5807	J4002	BIG DATA ANALYTICS	37	27	1
17231D5807	J5801	COMPUTER NETWORKS	33	24	1
17231D5807	J5803	CSE LAB 2	39	56	1
17231D5809	J0502	SOFTWARE ENGINEERING ELECTIVE I	36	27	1
17231D5809	J2503	CYBER SECURITY	38	26	1
17231D5809	J2510	CLOUD COMPUTING ELECTIVE II	37	31	1
17231D5809	J4001	ADVANCED UNIX PROGRAMMING	38	31	1
17231D5809	J4002	BIG DATA ANALYTICS	37	35	1
17231D5809	J5801	COMPUTER NETWORKS	39	28	1
17231D5809	J5803	CSE LAB 2	39	57	1
17231D5810	J0502	SOFTWARE ENGINEERING ELECTIVE I	33	28	1
17231D5810	J2503	CYBER SECURITY	36	30	1
17231D5810	J2510	CLOUD COMPUTING ELECTIVE II	37	38	1
17231D5810	J4001	ADVANCED UNIX PROGRAMMING	35	33	1
17231D5810	J4002	BIG DATA ANALYTICS	36	24	1
17231D5810	J5801	COMPUTER NETWORKS	36	24	1
17231D5810	J5803	CSE LAB 2	38	58	1
17231D5811	J0502	SOFTWARE ENGINEERING ELECTIVE I	35	25	1
17231D5811	J2503	CYBER SECURITY	38	28	1
17231D5811	J2510	CLOUD COMPUTING ELECTIVE II	39	26	1
17231D5811	J4001	ADVANCED UNIX PROGRAMMING	38	26	1
17231D5811	J4002	BIG DATA ANALYTICS	38	35	1
17231D5811	J5801	COMPUTER NETWORKS	37	28	1
17231D5811	J5803	CSE LAB 2	37	57	1
17231D5812	J0502	SOFTWARE ENGINEERING ELECTIVE I	35	24	1
17231D5812	J2503	CYBER SECURITY	38	32	1
17231D5812	J2510	CLOUD COMPUTING ELECTIVE II	39	26	1
17231D5812	J4001	ADVANCED UNIX PROGRAMMING	39	24	1
17231D5812	J4002	BIG DATA ANALYTICS	38	25	1
17231D5812	J5801	COMPUTER NETWORKS	36	27	1
17231D5812	J5803	CSE LAB 2	38	58	1
17231D6101	J5701	CAD FOR VLSI ELECTIVEIII	34	25	1
17231D6101	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	32	24	1
17231D6101	J5704	BACK END VLSI DESIGN LABORATORY	35	54	1
17231D6101	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	33	25	1
17231D6101	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	34	25	1
17231D6101	J6804	DESIGN FOR TESTABILITY	32	25	1
17231D6101	J6806	LOW POWER VLSI DESIGN	32	24	1
17231D6102	J5701	CAD FOR VLSI ELECTIVEIII	31	26	1
17231D6102	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	32	24	1
17231D6102	J5704	BACK END VLSI DESIGN LABORATORY	34	54	1
17231D6102	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	28	24	1
17231D6102	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	36	25	1
17231D6102	J6804	DESIGN FOR TESTABILITY	29	30	1

Htno	Subcode	Subname	Internal	External	credits
17231D6102	J6806	LOW POWER VLSI DESIGN	34	33	1
17231D6103	J5701	CAD FOR VLSI ELECTIVEIII	33	33	1
17231D6103	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	33	25	1
17231D6103	J5704	BACK END VLSI DESIGN LABORATORY	35	55	1
17231D6103	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	30	28	1
17231D6103	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	34	33	1
17231D6103	J6804	DESIGN FOR TESTABILITY	28	30	1
17231D6103	J6806	LOW POWER VLSI DESIGN	33	28	1
17231D6104	J5701	CAD FOR VLSI ELECTIVEIII	33	37	1
17231D6104	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	35	25	1
17231D6104	J5704	BACK END VLSI DESIGN LABORATORY	36	55	1
17231D6104	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	31	33	1
17231D6104	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	37	27	1
17231D6104	J6804	DESIGN FOR TESTABILITY	38	24	1
17231D6104	J6806	LOW POWER VLSI DESIGN	35	30	1
17231D6105	J5701	CAD FOR VLSI ELECTIVEIII	34	32	1
17231D6105	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	34	32	1
17231D6105	J5704	BACK END VLSI DESIGN LABORATORY	34	52	1
17231D6105	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	33	31	1
17231D6105	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	31	1
17231D6105	J6804	DESIGN FOR TESTABILITY	36	30	1
17231D6105	J6806	LOW POWER VLSI DESIGN	33	40	1
17231D6106	J5701	CAD FOR VLSI ELECTIVEIII	33	30	1
17231D6106	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	35	29	1
17231D6106	J5704	BACK END VLSI DESIGN LABORATORY	35	51	1
17231D6106	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	33	26	1
17231D6106	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	35	30	1
17231D6106	J6804	DESIGN FOR TESTABILITY	34	24	1
17231D6106	J6806	LOW POWER VLSI DESIGN	34	28	1
17231D6107	J5701	CAD FOR VLSI ELECTIVEIII	29	36	1
17231D6107	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	32	28	1
17231D6107	J5704	BACK END VLSI DESIGN LABORATORY	36	55	1
17231D6107	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	32	29	1
17231D6107	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	34	26	1
17231D6107	J6804	DESIGN FOR TESTABILITY	29	24	1
17231D6107	J6806	LOW POWER VLSI DESIGN	31	25	1
17231D6108	J5701	CAD FOR VLSI ELECTIVEIII	33	31	1
17231D6108	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	32	29	1
17231D6108	J5704	BACK END VLSI DESIGN LABORATORY	34	54	1
17231D6108	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	33	30	1
17231D6108	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	36	30	1
17231D6108	J6804	DESIGN FOR TESTABILITY	37	24	1
17231D6108	J6806	LOW POWER VLSI DESIGN	33	28	1
17231D6109	J5701	CAD FOR VLSI ELECTIVEIII	31	24	1
17231D6109	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	30	25	1
17231D6109	J5704	BACK END VLSI DESIGN LABORATORY	35	52	1
17231D6109	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	26	28	1
17231D6109	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	29	1
17231D6109	J6804	DESIGN FOR TESTABILITY	33	24	1
17231D6109	J6806	LOW POWER VLSI DESIGN	30	32	1
17231D6110	J5701	CAD FOR VLSI ELECTIVEIII	33	45	1

Htno	Subcode	Subname	Internal	External	credits
17231D6110	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	27	30	1
17231D6110	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	33	1
17231D6110	J6804	DESIGN FOR TESTABILITY	31	27	1
17231D6110	J6806	LOW POWER VLSI DESIGN	30	34	1
17231D6111	J5701	CAD FOR VLSI ELECTIVEIII	29	35	1
17231D6111	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	35	32	1
17231D6111	J5704	BACK END VLSI DESIGN LABORATORY	35	54	1
17231D6111	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	34	24	1
17231D6111	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	36	30	1
17231D6111	J6804	DESIGN FOR TESTABILITY	30	28	1
17231D6111	J6806	LOW POWER VLSI DESIGN	34	28	1
17231D6112	J5701	CAD FOR VLSI ELECTIVEIII	30	51	1
17231D6112	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	27	25	1
17231D6112	J5704	BACK END VLSI DESIGN LABORATORY	35	55	1
17231D6112	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	27	24	1
17231D6112	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	32	27	1
17231D6112	J6804	DESIGN FOR TESTABILITY	36	24	1
17231D6112	J6806	LOW POWER VLSI DESIGN	34	28	1
17231D6114	J5701	CAD FOR VLSI ELECTIVEIII	33	27	1
17231D6114	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	33	28	1
17231D6114	J5704	BACK END VLSI DESIGN LABORATORY	34	52	1
17231D6114	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	31	24	1
17231D6114	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	31	25	1
17231D6114	J6804	DESIGN FOR TESTABILITY	24	25	1
17231D6114	J6806	LOW POWER VLSI DESIGN	32	28	1

**Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through Online(www.jntukresults.edu.in)

NOTE:2 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **01-10-2018]

**NOTE:3 [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]

**NOTE:

[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject.]

Date:17-09-2018

N. Mohan Rao
Controller of Examinations