



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech II SEMESTER(R13/R16) Regular/Supplementary Examinations, MAY-2017.

College: NIMRA COLLEGE OF ENGG., JUPUDI, IBRAHIMPATNAM:23

Discrepancy pertaining to these results are to be submitted on or before 20-12-2017 with following documents at CE(PG) Office, JNTUK, Kakinada

Htno	Subcode	Subname	Internal	External	credits
13231D0407	H2103	FINITE ELEMENT METHOD	35	28	1
13231D0408	H2103	FINITE ELEMENT METHOD	36	13	0
13231D0412	H0402	COMPUTER GRAPHICS	37	24	1
13231D0415	H0402	COMPUTER GRAPHICS	35	27	1
13231D0416	H2103	FINITE ELEMENT METHOD	37	10	0
13231D0510	H0501	DATA WAREHOUSING AND DATA MINING	27	29	1
13231D0510	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	27	16	0
13231D4214	H4301	SWITCHED MODE POWER CONVERSION	35	2	0
13231D4214	H4303	DIGITAL CONTROLLERS	36	16	0
13231D5706	H6809	LOW POWER VLSI DESIGN	37	25	1
13231D5711	H6809	LOW POWER VLSI DESIGN	35	14	0
13231D5815	H4002	INFORMATION SECURITY	34	31	1
13232D5805	H4002	INFORMATION SECURITY	29	32	1
14231D0403	H2103	FINITE ELEMENT METHOD	35	29	1
14231D0404	H2103	FINITE ELEMENT METHOD	35	0	0
14231D0406	H2103	FINITE ELEMENT METHOD	36	17	0
14231D0413	H2103	FINITE ELEMENT METHOD	37	14	0
14231D0414	H0402	COMPUTER GRAPHICS	40	28	1
14231D0414	H2103	FINITE ELEMENT METHOD	38	8	0
14231D0502	H4002	INFORMATION SECURITY	35	30	1
14231D4202	H4301	SWITCHED MODE POWER CONVERSION	36	24	1
14231D5615	H5601	POWER SYSTEM DYNAMICS AND STABILITY	30	27	1
14231D5615	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	30	30	1
14231D5615	H5603	REAL TIME CONTROL OF POWER SYSTEMS	30	43	1
14231D5615	H5604	ADVANCED POWER SYSTEM PROTECTION	30	33	1
14231D5615	H5606	POWER QUALITY ELECTIVE-III	31	24	1
14231D5615	H5611	POWER SYSTEM TRANSIENTS ELECTIVE-IV	30	36	1
14231D5615	H5613	POWER SYSTEMS LAB	30	53	1
14231D5809	H4002	INFORMATION SECURITY	33	14	0
14231D5809	H5801	COMPUTER NETWORKS	33	24	1
14231D6108	H6806	SYSTEM ON CHIP DESIGN	33	26	1
14231D6110	H6806	SYSTEM ON CHIP DESIGN	35	26	1
14231D6110	H6809	LOW POWER VLSI DESIGN	34	34	1
14232D3609	H4503	WIRELESS COMMUNICATION & NETWORKS	29	15	0
14232D3609	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	7	0
14232D3610	H4505	RF CIRCUIT DESIGN ELECTIVE-III	30	16	0
14232D5805	H4002	INFORMATION SECURITY	37	29	1
14232D5807	H4002	INFORMATION SECURITY	35	30	1
14232D5808	H4002	INFORMATION SECURITY	36	20	0
14232D5811	H4002	INFORMATION SECURITY	32	24	1
14232D5812	H4002	INFORMATION SECURITY	31	17	0

Htno	Subcode	Subname	Internal	External	credits
15231D0402	H0401	SIMULATION MODELING OF MANUFACTURING SYS	34	-1	0
15231D0402	H0402	COMPUTER GRAPHICS	36	-1	0
15231D0402	H0404	CONCURRENT ENGINEERING ELECTIVE-III	35	-1	0
15231D0402	H0407	MATERIALS TECHNOLOGY	36	-1	0
15231D0402	H1501	OPTIMIZATION AND RELIABILITY	35	-1	0
15231D0402	H2103	FINITE ELEMENT METHOD	36	-1	0
15231D0403	H0402	COMPUTER GRAPHICS	37	30	1
15231D0405	H0402	COMPUTER GRAPHICS	37	24	1
15231D0405	H2103	FINITE ELEMENT METHOD	38	14	0
15231D4202	H4301	SWITCHED MODE POWER CONVERSION	33	-1	0
15231D4202	H4302	ELECTRIC DRIVES-II	33	-1	0
15231D4202	H4303	DIGITAL CONTROLLERS	34	-1	0
15231D4202	H4304	CUSTOM POWER DEVICES	33	-1	0
15231D4202	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	-1	0
15231D4202	H4308	SPECIAL MACHINES ELECTIVE-IV	33	-1	0
15231D4202	H4310	POWER CONVERTERS & DRIVES LAB	33	52	1
15231D4301	H4301	SWITCHED MODE POWER CONVERSION	37	27	1
15231D4301	H4308	SPECIAL MACHINES ELECTIVE-IV	37	27	1
15231D4302	H4301	SWITCHED MODE POWER CONVERSION	37	36	1
15231D4304	H4301	SWITCHED MODE POWER CONVERSION	30	-1	0
15231D4304	H4302	ELECTRIC DRIVES-II	30	-1	0
15231D4304	H4303	DIGITAL CONTROLLERS	31	-1	0
15231D4304	H4304	CUSTOM POWER DEVICES	30	-1	0
15231D4304	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	30	-1	0
15231D4304	H4308	SPECIAL MACHINES ELECTIVE-IV	32	-1	0
15231D4304	H4310	POWER CONVERTERS & DRIVES LAB	32	52	1
15231D4305	H4301	SWITCHED MODE POWER CONVERSION	37	10	0
15231D4305	H4304	CUSTOM POWER DEVICES	38	33	1
15231D4305	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	38	18	0
15231D4305	H4308	SPECIAL MACHINES ELECTIVE-IV	38	25	1
15231D5601	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	38	20	0
15231D5601	H5604	ADVANCED POWER SYSTEM PROTECTION	37	24	1
15231D5603	H5601	POWER SYSTEM DYNAMICS AND STABILITY	36	28	1
15231D5603	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	35	42	1
15231D5603	H5604	ADVANCED POWER SYSTEM PROTECTION	38	34	1
15231D5608	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	34	20	0
16231D0401	J0401	MODELING & SIMULATION OF MANUFACTURING S	14	-1	0
16231D0401	J0402	COMPUTER GRAPHICS	17	-1	0
16231D0401	J0405	CONCURRENT ENGINEERING ELECTIVEIII	17	-1	0
16231D0401	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	22	-1	0
16231D0401	J1501	OPTIMIZATION AND RELIABILITY	18	-1	0
16231D0401	J2103	FINITE ELEMENT METHODS	18	-1	0
16231D0401	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	18	-1	0
16231D0402	J0401	MODELING & SIMULATION OF MANUFACTURING S	36	-1	0
16231D0402	J0402	COMPUTER GRAPHICS	37	27	1
16231D0402	J0405	CONCURRENT ENGINEERING ELECTIVEIII	38	32	1
16231D0402	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	38	58	1
16231D0402	J1501	OPTIMIZATION AND RELIABILITY	37	9	0
16231D0402	J2103	FINITE ELEMENT METHODS	38	-1	0
16231D0402	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	38	25	1
16231D0403	J0401	MODELING & SIMULATION OF MANUFACTURING S	38	42	1

Htno	Subcode	Subname	Internal	External	credits
16231D0403	J0402	COMPUTER GRAPHICS	38	31	1
16231D0403	J0405	CONCURRENT ENGINEERING ELECTIVEIII	38	41	1
16231D0403	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	39	59	1
16231D0403	J1501	OPTIMIZATION AND RELIABILITY	39	27	1
16231D0403	J2103	FINITE ELEMENT METHODS	38	16	0
16231D0403	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	37	25	1
16231D0404	J0401	MODELING & SIMULATION OF MANUFACTURING S	0	-1	0
16231D0404	J0402	COMPUTER GRAPHICS	0	-1	0
16231D0404	J0405	CONCURRENT ENGINEERING ELECTIVEIII	0	-1	0
16231D0404	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	22	-1	0
16231D0404	J1501	OPTIMIZATION AND RELIABILITY	0	-1	0
16231D0404	J2103	FINITE ELEMENT METHODS	0	-1	0
16231D0404	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	0	-1	0
16231D0406	J0401	MODELING & SIMULATION OF MANUFACTURING S	20	-1	0
16231D0406	J0402	COMPUTER GRAPHICS	20	-1	0
16231D0406	J0405	CONCURRENT ENGINEERING ELECTIVEIII	20	-1	0
16231D0406	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	38	56	1
16231D0406	J1501	OPTIMIZATION AND RELIABILITY	20	-1	0
16231D0406	J2103	FINITE ELEMENT METHODS	20	-1	0
16231D0406	J2105	MATERIALS TECHNOLOGY ELECTIVEIV	20	-1	0
16231D4201	J4301	SWITCHED MODE POWER CONVERSION	34	29	1
16231D4201	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	33	12	0
16231D4201	J4303	DIGITAL CONTROLLERS	35	24	1
16231D4201	J4304	CUSTOM POWER DEVICES	33	24	1
16231D4201	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	32	11	0
16231D4201	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	32	11	0
16231D4201	J4310	POWER CONVERTERS & DRIVES LABORATORY	34	57	1
16231D4202	J4301	SWITCHED MODE POWER CONVERSION	0	-1	0
16231D4202	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	0	-1	0
16231D4202	J4303	DIGITAL CONTROLLERS	0	-1	0
16231D4202	J4304	CUSTOM POWER DEVICES	0	-1	0
16231D4202	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	0	-1	0
16231D4202	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	0	-1	0
16231D4202	J4310	POWER CONVERTERS & DRIVES LABORATORY	0	-1	0
16231D4203	J4301	SWITCHED MODE POWER CONVERSION	36	1	0
16231D4203	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	34	0	0
16231D4203	J4303	DIGITAL CONTROLLERS	34	12	0
16231D4203	J4304	CUSTOM POWER DEVICES	36	13	0
16231D4203	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	-1	0
16231D4203	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	36	8	0
16231D4203	J4310	POWER CONVERTERS & DRIVES LABORATORY	36	59	1
16231D4301	J4301	SWITCHED MODE POWER CONVERSION	34	-1	0
16231D4301	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	36	0	0
16231D4301	J4303	DIGITAL CONTROLLERS	35	15	0
16231D4301	J4304	CUSTOM POWER DEVICES	35	-1	0
16231D4301	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	-1	0
16231D4301	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	34	-1	0
16231D4301	J4310	POWER CONVERTERS & DRIVES LABORATORY	38	59	1
16231D4302	J4301	SWITCHED MODE POWER CONVERSION	36	-1	0
16231D4302	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	35	10	0
16231D4302	J4303	DIGITAL CONTROLLERS	34	12	0

Htno	Subcode	Subname	Internal	External	credits
16231D4302	J4304	CUSTOM POWER DEVICES	35	28	1
16231D4302	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	-1	0
16231D4302	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	34	-1	0
16231D4302	J4310	POWER CONVERTERS & DRIVES LABORATORY	37	59	1
16231D4303	J4301	SWITCHED MODE POWER CONVERSION	33	12	0
16231D4303	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	34	8	0
16231D4303	J4303	DIGITAL CONTROLLERS	35	15	0
16231D4303	J4304	CUSTOM POWER DEVICES	34	26	1
16231D4303	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	33	16	0
16231D4303	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	33	30	1
16231D4303	J4310	POWER CONVERTERS & DRIVES LABORATORY	36	56	1
16231D4304	J4301	SWITCHED MODE POWER CONVERSION	37	24	1
16231D4304	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	37	24	1
16231D4304	J4303	DIGITAL CONTROLLERS	35	24	1
16231D4304	J4304	CUSTOM POWER DEVICES	36	24	1
16231D4304	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	37	13	0
16231D4304	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	37	28	1
16231D4304	J4310	POWER CONVERTERS & DRIVES LABORATORY	38	58	1
16231D4305	J4301	SWITCHED MODE POWER CONVERSION	34	18	0
16231D4305	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	36	24	1
16231D4305	J4303	DIGITAL CONTROLLERS	35	24	1
16231D4305	J4304	CUSTOM POWER DEVICES	34	11	0
16231D4305	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	36	14	0
16231D4305	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	34	33	1
16231D4305	J4310	POWER CONVERTERS & DRIVES LABORATORY	36	57	1
16231D4306	J4301	SWITCHED MODE POWER CONVERSION	35	24	1
16231D4306	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	34	28	1
16231D4306	J4303	DIGITAL CONTROLLERS	33	36	1
16231D4306	J4304	CUSTOM POWER DEVICES	32	30	1
16231D4306	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	17	0
16231D4306	J4309	PROGRAMMABLE LOGIC CONTROLLERS & APPLICA	33	26	1
16231D4306	J4310	POWER CONVERTERS & DRIVES LABORATORY	37	58	1
16231D5801	J0502	SOFTWARE ENGINEERING ELECTIVE I	33	24	1
16231D5801	J2503	CYBER SECURITY	30	0	0
16231D5801	J2510	CLOUD COMPUTING ELECTIVE II	33	3	0
16231D5801	J4001	ADVANCED UNIX PROGRAMMING	32	0	0
16231D5801	J4002	BIG DATA ANALYTICS	33	14	0
16231D5801	J5801	COMPUTER NETWORKS	32	16	0
16231D5801	J5803	CSE LAB 2	38	57	1
16231D5802	J0502	SOFTWARE ENGINEERING ELECTIVE I	37	24	1
16231D5802	J2503	CYBER SECURITY	37	24	1
16231D5802	J2510	CLOUD COMPUTING ELECTIVE II	38	13	0
16231D5802	J4001	ADVANCED UNIX PROGRAMMING	38	24	1
16231D5802	J4002	BIG DATA ANALYTICS	38	24	1
16231D5802	J5801	COMPUTER NETWORKS	37	20	0
16231D5802	J5803	CSE LAB 2	39	59	1
16231D5803	J0502	SOFTWARE ENGINEERING ELECTIVE I	37	28	1
16231D5803	J2503	CYBER SECURITY	31	11	0
16231D5803	J2510	CLOUD COMPUTING ELECTIVE II	36	19	0
16231D5803	J4001	ADVANCED UNIX PROGRAMMING	35	6	0
16231D5803	J4002	BIG DATA ANALYTICS	38	9	0

Htno	Subcode	Subname	Internal	External	credits
16231D5803	J5801	COMPUTER NETWORKS	32	12	0
16231D5803	J5803	CSE LAB 2	37	56	1
16231D5804	J0502	SOFTWARE ENGINEERING ELECTIVE I	35	24	1
16231D5804	J2503	CYBER SECURITY	35	6	0
16231D5804	J2510	CLOUD COMPUTING ELECTIVE II	36	7	0
16231D5804	J4001	ADVANCED UNIX PROGRAMMING	37	24	1
16231D5804	J4002	BIG DATA ANALYTICS	39	16	0
16231D5804	J5801	COMPUTER NETWORKS	36	8	0
16231D5804	J5803	CSE LAB 2	38	58	1
16231D5805	J0502	SOFTWARE ENGINEERING ELECTIVE I	30	24	1
16231D5805	J2503	CYBER SECURITY	27	0	0
16231D5805	J2510	CLOUD COMPUTING ELECTIVE II	30	7	0
16231D5805	J4001	ADVANCED UNIX PROGRAMMING	34	19	0
16231D5805	J4002	BIG DATA ANALYTICS	32	8	0
16231D5805	J5801	COMPUTER NETWORKS	29	10	0
16231D5805	J5803	CSE LAB 2	37	57	1
16231D5807	J0502	SOFTWARE ENGINEERING ELECTIVE I	32	24	1
16231D5807	J2503	CYBER SECURITY	31	28	1
16231D5807	J2510	CLOUD COMPUTING ELECTIVE II	30	24	1
16231D5807	J4001	ADVANCED UNIX PROGRAMMING	35	16	0
16231D5807	J4002	BIG DATA ANALYTICS	35	8	0
16231D5807	J5801	COMPUTER NETWORKS	31	24	1
16231D5807	J5803	CSE LAB 2	37	58	1
16231D5808	J0502	SOFTWARE ENGINEERING ELECTIVE I	38	26	1
16231D5808	J2503	CYBER SECURITY	33	27	1
16231D5808	J2510	CLOUD COMPUTING ELECTIVE II	36	19	0
16231D5808	J4001	ADVANCED UNIX PROGRAMMING	37	28	1
16231D5808	J4002	BIG DATA ANALYTICS	38	24	1
16231D5808	J5801	COMPUTER NETWORKS	35	35	1
16231D5808	J5803	CSE LAB 2	38	57	1
16231D5810	J0502	SOFTWARE ENGINEERING ELECTIVE I	39	41	1
16231D5810	J2503	CYBER SECURITY	32	28	1
16231D5810	J2510	CLOUD COMPUTING ELECTIVE II	37	26	1
16231D5810	J4001	ADVANCED UNIX PROGRAMMING	37	32	1
16231D5810	J4002	BIG DATA ANALYTICS	39	28	1
16231D5810	J5801	COMPUTER NETWORKS	37	35	1
16231D5810	J5803	CSE LAB 2	39	59	1
16231D5811	J0502	SOFTWARE ENGINEERING ELECTIVE I	37	25	1
16231D5811	J2503	CYBER SECURITY	34	16	0
16231D5811	J2510	CLOUD COMPUTING ELECTIVE II	38	7	0
16231D5811	J4001	ADVANCED UNIX PROGRAMMING	36	24	1
16231D5811	J4002	BIG DATA ANALYTICS	39	17	0
16231D5811	J5801	COMPUTER NETWORKS	37	12	0
16231D5811	J5803	CSE LAB 2	39	59	1
16231D5812	J0502	SOFTWARE ENGINEERING ELECTIVE I	14	-1	0
16231D5812	J2503	CYBER SECURITY	14	-1	0
16231D5812	J2510	CLOUD COMPUTING ELECTIVE II	14	-1	0
16231D5812	J4001	ADVANCED UNIX PROGRAMMING	14	-1	0
16231D5812	J4002	BIG DATA ANALYTICS	14	-1	0
16231D5812	J5801	COMPUTER NETWORKS	14	-1	0
16231D5812	J5803	CSE LAB 2	20	-1	0

Htno	Subcode	Subname	Internal	External	credits
16231D6101	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	36	24	1
16231D6101	J5704	BACK END VLSI DESIGN LABORATORY	37	57	1
16231D6101	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	33	34	1
16231D6101	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	31	5	0
16231D6101	J6804	DESIGN FOR TESTABILITY	35	15	0
16231D6101	J6805	DSP PROCESSORS AND ARCHITECTURES	35	24	1
16231D6101	J6806	LOW POWER VLSI DESIGN	37	28	1
16231D6102	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	36	39	1
16231D6102	J5704	BACK END VLSI DESIGN LABORATORY	38	55	1
16231D6102	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	32	41	1
16231D6102	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	31	34	1
16231D6102	J6804	DESIGN FOR TESTABILITY	33	28	1
16231D6102	J6805	DSP PROCESSORS AND ARCHITECTURES	36	44	1
16231D6102	J6806	LOW POWER VLSI DESIGN	35	32	1
16231D6103	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	34	35	1
16231D6103	J5704	BACK END VLSI DESIGN LABORATORY	39	58	1
16231D6103	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	33	26	1
16231D6103	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	24	1
16231D6103	J6804	DESIGN FOR TESTABILITY	36	17	0
16231D6103	J6805	DSP PROCESSORS AND ARCHITECTURES	37	8	0
16231D6103	J6806	LOW POWER VLSI DESIGN	36	24	1
16231D6104	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	34	26	1
16231D6104	J5704	BACK END VLSI DESIGN LABORATORY	35	56	1
16231D6104	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	37	31	1
16231D6104	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	27	1
16231D6104	J6804	DESIGN FOR TESTABILITY	36	27	1
16231D6104	J6805	DSP PROCESSORS AND ARCHITECTURES	34	15	0
16231D6104	J6806	LOW POWER VLSI DESIGN	37	28	1
16231D6105	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	34	30	1
16231D6105	J5704	BACK END VLSI DESIGN LABORATORY	35	57	1
16231D6105	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	39	43	1
16231D6105	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	37	32	1
16231D6105	J6804	DESIGN FOR TESTABILITY	35	31	1
16231D6105	J6805	DSP PROCESSORS AND ARCHITECTURES	36	32	1
16231D6105	J6806	LOW POWER VLSI DESIGN	37	40	1
16231D6106	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	37	29	1
16231D6106	J5704	BACK END VLSI DESIGN LABORATORY	38	54	1
16231D6106	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	37	35	1
16231D6106	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	31	33	1
16231D6106	J6804	DESIGN FOR TESTABILITY	37	27	1
16231D6106	J6805	DSP PROCESSORS AND ARCHITECTURES	34	24	1
16231D6106	J6806	LOW POWER VLSI DESIGN	35	36	1
16231D6107	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	37	25	1
16231D6107	J5704	BACK END VLSI DESIGN LABORATORY	38	58	1
16231D6107	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	34	33	1
16231D6107	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	35	36	1
16231D6107	J6804	DESIGN FOR TESTABILITY	34	35	1
16231D6107	J6805	DSP PROCESSORS AND ARCHITECTURES	35	24	1
16231D6107	J6806	LOW POWER VLSI DESIGN	35	30	1
16231D6108	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	33	26	1
16231D6108	J5704	BACK END VLSI DESIGN LABORATORY	35	55	1

Htno	Subcode	Subname	Internal	External	credits
16231D6108	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	34	35	1
16231D6108	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	31	28	1
16231D6108	J6804	DESIGN FOR TESTABILITY	37	12	0
16231D6108	J6805	DSP PROCESSORS AND ARCHITECTURES	34	24	1
16231D6108	J6806	LOW POWER VLSI DESIGN	35	29	1
16231D6109	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	34	25	1
16231D6109	J5704	BACK END VLSI DESIGN LABORATORY	36	54	1
16231D6109	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	31	36	1
16231D6109	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	31	36	1
16231D6109	J6804	DESIGN FOR TESTABILITY	33	29	1
16231D6109	J6805	DSP PROCESSORS AND ARCHITECTURES	34	35	1
16231D6109	J6806	LOW POWER VLSI DESIGN	34	24	1
16231D6110	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	31	28	1
16231D6110	J5704	BACK END VLSI DESIGN LABORATORY	37	56	1
16231D6110	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	34	33	1
16231D6110	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	35	32	1
16231D6110	J6804	DESIGN FOR TESTABILITY	36	27	1
16231D6110	J6805	DSP PROCESSORS AND ARCHITECTURES	34	25	1
16231D6110	J6806	LOW POWER VLSI DESIGN	36	30	1
16231D6111	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	33	33	1
16231D6111	J5704	BACK END VLSI DESIGN LABORATORY	36	55	1
16231D6111	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	37	34	1
16231D6111	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	36	27	1
16231D6111	J6804	DESIGN FOR TESTABILITY	38	33	1
16231D6111	J6805	DSP PROCESSORS AND ARCHITECTURES	36	24	1
16231D6111	J6806	LOW POWER VLSI DESIGN	36	31	1
16231D6112	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	33	-1	0
16231D6112	J5704	BACK END VLSI DESIGN LABORATORY	32	-1	0
16231D6112	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	33	-1	0
16231D6112	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	31	-1	0
16231D6112	J6804	DESIGN FOR TESTABILITY	32	-1	0
16231D6112	J6805	DSP PROCESSORS AND ARCHITECTURES	33	-1	0
16231D6112	J6806	LOW POWER VLSI DESIGN	34	-1	0
16231D6113	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	36	24	1
16231D6113	J5704	BACK END VLSI DESIGN LABORATORY	37	54	1
16231D6113	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	36	26	1
16231D6113	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	35	32	1
16231D6113	J6804	DESIGN FOR TESTABILITY	37	16	0
16231D6113	J6805	DSP PROCESSORS AND ARCHITECTURES	37	11	0
16231D6113	J6806	LOW POWER VLSI DESIGN	37	26	1
16231D6114	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	35	26	1
16231D6114	J5704	BACK END VLSI DESIGN LABORATORY	35	56	1
16231D6114	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	36	33	1
16231D6114	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	34	1
16231D6114	J6804	DESIGN FOR TESTABILITY	31	31	1
16231D6114	J6805	DSP PROCESSORS AND ARCHITECTURES	36	52	1
16231D6114	J6806	LOW POWER VLSI DESIGN	34	34	1
16231D6115	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	17	-1	0
16231D6115	J5704	BACK END VLSI DESIGN LABORATORY	35	56	1
16231D6115	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	15	-1	0
16231D6115	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	15	12	0

Htno	Subcode	Subname	Internal	External	credits
16231D6115	J6804	DESIGN FOR TESTABILITY	17	18	0
16231D6115	J6805	DSP PROCESSORS AND ARCHITECTURES	18	-1	0
16231D6115	J6806	LOW POWER VLSI DESIGN	18	-1	0

**\*\*Note:1)**For Recounting/Revaluation/Challenge By Revaluation Apply through Online([www.jntukresults.edu.in](http://www.jntukresults.edu.in))

**\*\*NOTE:2** [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **27-12-2017**]

**\*\*NOTE:3** [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation ]

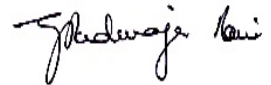
**\*\*NOTE:**

[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject. ]

Date:06-12-2017



Controller of Examinations