JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech (R16/R13) I Semester Regular /Supplementary Examinations, JANUARY-2018 .

College: NIMRA COLLEGE OF ENGG., JUPUDI, IBRAHIMPATNAM:23

Discrepancy pertaining to these results are to be submitted on or before 02-05-2018 with following documents at CE(PG) Office,JNTUK,Kakinada

Htno	Subcode	Subname	Internal	External	credits
14231D0414	G0401	INDUSTRIAL ROBOTICS	37	29	1
14231D4203	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	35	31	1
14231D4204	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	35	32	1
14231D4309	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	34	-1	0
14231D6108	G6802	VLSI TECHNOLOGY AND DESIGN	33	34	1
15231D4202	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	32	28	1
15231D4202	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	32	36	1
15231D4202	G4305	POWER QUALITY	32	17	0
15231D4301	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	28	1
15231D4301	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	39	36	1
15231D4302	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	36	28	1
16231D0402	I0402	COMPUTER AIDED MANUFACTURING	39	28	1
16231D0406	I0402	COMPUTER AIDED MANUFACTURING	38	27	1
16231D0406	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	36	24	1
16231D0406	I1506	GEOMETRIC MODELLING ELECTIVE 1	34	27	1
16231D4201	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	30	28	1
16231D4201	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	35	16	0
16231D4201	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	34	24	1
16231D4201	I5602	HVDC TRANSMISSION	37	28	1
16231D4203	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	34	-1	0
16231D4203	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	37	-1	0
16231D4203	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	35	-1	0
16231D4203	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	39	-1	0
16231D4203	I4305	POWER QUALITY	38	-1	0
16231D4203	I5602	HVDC TRANSMISSION	38	-1	0
16231D4301	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	36	-1	0
16231D4301	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	38	24	1
16231D4301	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	34	-1	0
16231D4301	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	35	-1	0
16231D4301	I4305	POWER QUALITY	37	-1	0
16231D4301	I5602	HVDC TRANSMISSION	37	-1	0
16231D4302	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	36	-1	0
16231D4302	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	39	-1	0
16231D4302	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	34	-1	0
16231D4302	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	36	-1	0
16231D4302	I4305	POWER QUALITY	37	-1	0
16231D4302	I5602	HVDC TRANSMISSION	37	-1	0
16231D4303	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	35	24	1
16231D4303	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	34	28	1
16231D4303	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	33	24	1
16231D4304	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	26	1

Htno	Subcode	Subname	Internal	External	credits
16231D4304	I4305	POWER QUALITY	34	28	1
16231D4304	I5602	HVDC TRANSMISSION	36	32	1
16231D4305	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	34	36	1
16231D5801	I0504	ADVANCED OPERATING SYSTEM	38	-1	0
16231D5801	I0505	DATA WAREHOUSING AND DATA MINING	32	-1	0
16231D5801	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	28	-1	0
16231D5801	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	35	-1	0
16231D5801	I5803	DATABASE MANAGEMENT SYSTEMS	34	-1	0
16231D5802	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	38	14	0
16231D5803	I0504	ADVANCED OPERATING SYSTEM	34	-1	0
16231D5803	I0505	DATA WAREHOUSING AND DATA MINING	28	-1	0
16231D5803	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	34	-1	0
16231D5804	I0504	ADVANCED OPERATING SYSTEM	39	24	1
16231D5804	I0505	DATA WAREHOUSING AND DATA MINING	37	8	0
16231D5804	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	-1	0
16231D5807	I0505	DATA WAREHOUSING AND DATA MINING	34	-1	0
16231D5807	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	38	-1	0
16231D5807	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	33	-1	0
16231D5807	I5803	DATABASE MANAGEMENT SYSTEMS	38	-1	0
16231D5811	I0504	ADVANCED OPERATING SYSTEM	38	24	1
16231D5811	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	38	-1	0
16231D6101	I6801	DIGITAL SYSTEM DESIGN	35	6	0
16231D6102	I6801	DIGITAL SYSTEM DESIGN	38	28	1
16231D6103	I6801	DIGITAL SYSTEM DESIGN	36	17	0
16231D6104	I6803	CMOS ANALOG IC DESIGN	32	35	1
16231D6105	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	36	23	0
16231D6106	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	36	25	1
16231D6107	I6803	CMOS ANALOG IC DESIGN	32	46	1
16231D6107	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	31	1
16231D6108	I6803	CMOS ANALOG IC DESIGN	31	50	1
16231D6108	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	26	1
16231D6109	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	32	24	1
16231D6109	I6803	CMOS ANALOG IC DESIGN	31	44	1
16231D6109	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	32	33	1
16231D6111	I6803	CMOS ANALOG IC DESIGN	33	33	1
16231D6111	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	34	1
16231D6113	I6801	DIGITAL SYSTEM DESIGN	34	-1	0
16231D6113	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	36	-1	0
16231D6113	I6803	CMOS ANALOG IC DESIGN	36	-1	0
16231D6113	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	35	-1	0
17231D0401	I0401	INDUSTRIAL ROBOTICS	37	35	1
17231D0401	I0402	COMPUTER AIDED MANUFACTURING	36	30	1
17231D0401	I0403	SPECIAL MANUFACTURING PROCESSES	38	26	1
17231D0401	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	35	24	1
17231D0401	I0407	ADVANCED CAD LAB	37	51	1
17231D0401	I1506	GEOMETRIC MODELLING ELECTIVE 1	36	33	1
17231D0401	I1809	NANO TECHNOLOGY ELECTIVE 2	36	37	1
17231D0402	I0401	INDUSTRIAL ROBOTICS	36	41	1
17231D0402	I0402	COMPUTER AIDED MANUFACTURING	34	42	1
17231D0402	I0403	SPECIAL MANUFACTURING PROCESSES	38	36	1
17231D0402	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	35	28	1

Htno	Subcode	Subname	Internal	External	credits
17231D0402	I0407	ADVANCED CAD LAB	37	55	1
17231D0402	I1506	GEOMETRIC MODELLING ELECTIVE 1	36	34	1
17231D0402	I1809	NANO TECHNOLOGY ELECTIVE 2	37	41	1
17231D0403	I0401	INDUSTRIAL ROBOTICS	37	32	1
17231D0403	I0402	COMPUTER AIDED MANUFACTURING	36	28	1
17231D0403	I0403	SPECIAL MANUFACTURING PROCESSES	34	25	1
17231D0403	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	38	15	0
17231D0403	I0407	ADVANCED CAD LAB	36	51	1
17231D0403	I1506	GEOMETRIC MODELLING ELECTIVE 1	35	44	1
17231D0403	I1809	NANO TECHNOLOGY ELECTIVE 2	36	30	1
17231D0404	I0401	INDUSTRIAL ROBOTICS	34	36	1
17231D0404	I0402	COMPUTER AIDED MANUFACTURING	35	24	1
17231D0404	I0403	SPECIAL MANUFACTURING PROCESSES	34	24	1
17231D0404	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	38	16	0
17231D0404	I0407	ADVANCED CAD LAB	37	56	1
17231D0404	I1506	GEOMETRIC MODELLING ELECTIVE 1	37	16	0
17231D0404	I1809	NANO TECHNOLOGY ELECTIVE 2	36	31	1
17231D0405	I0401	INDUSTRIAL ROBOTICS	36	43	1
17231D0405	I0402	COMPUTER AIDED MANUFACTURING	37	46	1
17231D0405	I0403	SPECIAL MANUFACTURING PROCESSES	35	31	1
17231D0405	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	38	27	1
17231D0405	I0407	ADVANCED CAD LAB	38	57	1
17231D0405	I1506	GEOMETRIC MODELLING ELECTIVE 1	37	38	1
17231D0405	I1809	NANO TECHNOLOGY ELECTIVE 2	38	45	1
17231D0406	I0401	INDUSTRIAL ROBOTICS	39	29	1
17231D0406	I0402	COMPUTER AIDED MANUFACTURING	39	37	1
17231D0406	I0403	SPECIAL MANUFACTURING PROCESSES	38	31	1
17231D0406	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	39	32	1
17231D0406	I0407	ADVANCED CAD LAB	39	58	1
17231D0406	I1506	GEOMETRIC MODELLING ELECTIVE 1	38	33	1
17231D0406	I1809	NANO TECHNOLOGY ELECTIVE 2	38	33	1
17231D0407	I0401	INDUSTRIAL ROBOTICS	17	-1	0
17231D0407	I0402	COMPUTER AIDED MANUFACTURING	16	-1	0
17231D0407	I0403	SPECIAL MANUFACTURING PROCESSES	17	-1	0
17231D0407	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	19	-1	0
17231D0407	I0407	ADVANCED CAD LAB	22	-1	0
17231D0407	I1506	GEOMETRIC MODELLING ELECTIVE 1	19	-1	0
17231D0407	I1809	NANO TECHNOLOGY ELECTIVE 2	19	-1	0
17231D0408	I0401	INDUSTRIAL ROBOTICS	16	-1	0
17231D0408	I0402	COMPUTER AIDED MANUFACTURING	19	-1	0
17231D0408	I0403	SPECIAL MANUFACTURING PROCESSES	19	-1	0
17231D0408	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	17	-1	0
17231D0408	I0407	ADVANCED CAD LAB	21	-1	0
17231D0408	I1506	GEOMETRIC MODELLING ELECTIVE 1	17	-1	0
17231D0408	I1809	NANO TECHNOLOGY ELECTIVE 2	17	-1	0
17231D0409	I0401	INDUSTRIAL ROBOTICS	38	45	1
17231D0409	I0402	COMPUTER AIDED MANUFACTURING	37	45	1
17231D0409	I0403	SPECIAL MANUFACTURING PROCESSES	37	36	1
17231D0409	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	36	34	1
17231D0409	I0407	ADVANCED CAD LAB	38	56	1
17231D0409	I1506	GEOMETRIC MODELLING ELECTIVE 1	36	36	1

Htno	Subcode	Subname	Internal	External	credits
17231D0409	I1809	NANO TECHNOLOGY ELECTIVE 2	37	48	1
17231D0410	I0401	INDUSTRIAL ROBOTICS	37	46	1
17231D0410	I0402	COMPUTER AIDED MANUFACTURING	37	47	1
17231D0410	I0403	SPECIAL MANUFACTURING PROCESSES	35	39	1
17231D0410	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	38	32	1
17231D0410	I0407	ADVANCED CAD LAB	38	53	1
17231D0410	I1506	GEOMETRIC MODELLING ELECTIVE 1	38	49	1
17231D0410	I1809	NANO TECHNOLOGY ELECTIVE 2	37	50	1
17231D0411	I0401	INDUSTRIAL ROBOTICS	33	-1	0
17231D0411	I0402	COMPUTER AIDED MANUFACTURING	33	-1	0
17231D0411	I0403	SPECIAL MANUFACTURING PROCESSES	35	-1	0
17231D0411	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	36	-1	0
17231D0411	I0407	ADVANCED CAD LAB	35	49	1
17231D0411	I1506	GEOMETRIC MODELLING ELECTIVE 1	35	-1	0
17231D0411	I1809	NANO TECHNOLOGY ELECTIVE 2	36	-1	0
17231D0412	I0401	INDUSTRIAL ROBOTICS	18	-1	0
17231D0412	I0402	COMPUTER AIDED MANUFACTURING	17	-1	0
17231D0412	I0403	SPECIAL MANUFACTURING PROCESSES	33	-1	0
17231D0412	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	37	-1	0
17231D0412	I0407	ADVANCED CAD LAB	21	-1	0
17231D0412	I1506	GEOMETRIC MODELLING ELECTIVE 1	33	-1	0
17231D0412	I1809	NANO TECHNOLOGY ELECTIVE 2	37	-1	0
17231D4301	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	36	1
17231D4301	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	38	15	0
17231D4301	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	39	28	1
17231D4301	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	37	38	1
17231D4301	I4305	POWER QUALITY	39	26	1
17231D4301	I4309	SIMULATION LAB	40	58	1
17231D4301	I5602	HVDC TRANSMISSION	39	26	1
17231D4302	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	28	26	1
17231D4302	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	27	15	0
17231D4302	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	27	13	0
17231D4302	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	28	32	1
17231D4302	I4305	POWER QUALITY	27	13	0
17231D4302	I4309	SIMULATION LAB	35	55	1
17231D4302	I5602	HVDC TRANSMISSION	29	30	1
17231D4303	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	37	32	1
17231D4303	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	37	26	1
17231D4303	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	38	35	1
17231D4303	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	38	44	1
17231D4303	I4305	POWER QUALITY	38	31	1
17231D4303	I4309	SIMULATION LAB	38	56	1
17231D4303	I5602	HVDC TRANSMISSION	39	28	1
17231D4304	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	27	1
17231D4304	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	38	15	0
17231D4304	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	39	24	1
17231D4304	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	38	30	1
17231D4304	I4305	POWER QUALITY	38	24	1
17231D4304	I4309	SIMULATION LAB	40	57	1
17231D4304	I5602	HVDC TRANSMISSION	38	24	1
17231D4305	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	36	33	1

Htno	Subcode	Subname	Internal	External	credits
17231D4305	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	38	24	1
17231D4305	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	37	37	1
17231D4305	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	37	42	1
17231D4305	I4305	POWER QUALITY	38	27	1
17231D4305	I4309	SIMULATION LAB	38	57	1
17231D4305	I5602	HVDC TRANSMISSION	37	38	1
17231D4306	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	24	1
17231D4306	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	39	24	1
17231D4306	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	38	30	1
17231D4306	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	37	26	1
17231D4306	I4305	POWER QUALITY	38	24	1
17231D4306	I4309	SIMULATION LAB	39	58	1
17231D4306	I5602	HVDC TRANSMISSION	38	36	1
17231D4307	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	37	38	1
17231D4307	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	38	30	1
17231D4307	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	38	31	1
17231D4307	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	38	50	1
17231D4307	I4305	POWER QUALITY	38	36	1
17231D4307	I4309	SIMULATION LAB	39	58	1
17231D4307	I5602	HVDC TRANSMISSION	38	36	1
17231D4308	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	37	1
17231D4308	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	38	28	1
17231D4308	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	38	32	1
17231D4308	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	38	42	1
17231D4308	I4305	POWER QUALITY	37	25	1
17231D4308	I4309	SIMULATION LAB	40	58	1
17231D4308	I5602	HVDC TRANSMISSION	39	40	1
17231D5801	I0504	ADVANCED OPERATING SYSTEM	35	34	1
17231D5801	I0505	DATA WAREHOUSING AND DATA MINING	38	29	1
17231D5801	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	40	14	0
17231D5801	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	39	28	1
17231D5801	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	37	30	1
17231D5801	I5803	DATABASE MANAGEMENT SYSTEMS	38	31	1
17231D5801	I5805	CSE LAB 1	38	57	1
17231D5802	I0504	ADVANCED OPERATING SYSTEM	26	32	1
17231D5802	I0505	DATA WAREHOUSING AND DATA MINING	27	33	1
17231D5802	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	25	13	0
17231D5802	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	22	0	0
17231D5802	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	26	24	1
17231D5802	I5803	DATABASE MANAGEMENT SYSTEMS	26	-1	0
17231D5802	I5805	CSE LAB 1	34	52	1
17231D5803	I0504	ADVANCED OPERATING SYSTEM	24	-1	0
17231D5803	I0505	DATA WAREHOUSING AND DATA MINING	29	-1	0
17231D5803	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	26	-1	0
17231D5803	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	23	-1	0
17231D5803	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	27	-1	0
17231D5803	I5803	DATABASE MANAGEMENT SYSTEMS	26	-1	0
17231D5803	I5805	CSE LAB 1	32	50	1
17231D5804	I0504	ADVANCED OPERATING SYSTEM	32	24	1
17231D5804	I0505	DATA WAREHOUSING AND DATA MINING	35	17	0
17231D5804	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	34	15	0

Htno	Subcode	Subname	Internal	External	credits
17231D5804	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	38	25	1
17231D5804	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	34	30	1
17231D5804	I5803	DATABASE MANAGEMENT SYSTEMS	38	24	1
17231D5804	I5805	CSE LAB 1	39	58	1
17231D5805	I0504	ADVANCED OPERATING SYSTEM	36	29	1
17231D5805	I0505	DATA WAREHOUSING AND DATA MINING	39	31	1
17231D5805	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	40	7	0
17231D5805	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	32	1
17231D5805	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	39	26	1
17231D5805	I5803	DATABASE MANAGEMENT SYSTEMS	39	31	1
17231D5805	I5805	CSE LAB 1	39	59	1
17231D5806	I0504	ADVANCED OPERATING SYSTEM	33	28	1
17231D5806	I0505	DATA WAREHOUSING AND DATA MINING	36	4	0
17231D5806	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	30	0	0
17231D5806	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	30	24	1
17231D5806	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	30	26	1
17231D5806	I5803	DATABASE MANAGEMENT SYSTEMS	34	29	1
17231D5806	I5805	CSE LAB 1	36	56	1
17231D5807	I0504	ADVANCED OPERATING SYSTEM	33	27	1
17231D5807	I0505	DATA WAREHOUSING AND DATA MINING	29	25	1
17231D5807	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	24	1
17231D5807	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	34	33	1
17231D5807	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	37	36	1
17231D5807	I5803	DATABASE MANAGEMENT SYSTEMS	33	25	1
17231D5807	I5805	CSE LAB 1	34	53	1
17231D5809	I0504	ADVANCED OPERATING SYSTEM	32	34	1
17231D5809	I0505	DATA WAREHOUSING AND DATA MINING	33	31	1
17231D5809	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	39	24	1
17231D5809	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	39	36	1
17231D5809	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	37	28	1
17231D5809	I5803	DATABASE MANAGEMENT SYSTEMS	36	31	1
17231D5809	I5805	CSE LAB 1	38	58	1
17231D5810	I0504	ADVANCED OPERATING SYSTEM	34	45	1
17231D5810	I0505	DATA WAREHOUSING AND DATA MINING	35	37	1
17231D5810	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	37	15	0
17231D5810	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	37	29	1
17231D5810	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	37	26	1
17231D5810	I5803	DATABASE MANAGEMENT SYSTEMS	38	24	1
17231D5810	I5805	CSE LAB 1	39	59	1
17231D5811	I0504	ADVANCED OPERATING SYSTEM	32	25	1
17231D5811	I0505	DATA WAREHOUSING AND DATA MINING	35	31	1
17231D5811	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	37	8	0
17231D5811	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	35	25	1
17231D5811	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	37	34	1
17231D5811	I5803	DATABASE MANAGEMENT SYSTEMS	38	34	1
17231D5811	I5805	CSE LAB 1	38	57	1
17231D5812	I0504	ADVANCED OPERATING SYSTEM	34	36	1
17231D5812	I0505	DATA WAREHOUSING AND DATA MINING	38	32	1
17231D5812	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	38	14	0
17231D5812	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	39	28	1
17231D5812	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	39	30	1

Htno	Subcode	Subname	Internal	External	credits
17231D5812	I5803	DATABASE MANAGEMENT SYSTEMS	38	35	1
17231D5812	I5805	CSE LAB 1	37	58	1
17231D6101	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	34	15	0
17231D6101	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	27	1
17231D6101	I5704	FRONT END VLSI DESIGN LABORATORY	37	54	1
17231D6101	I6801	DIGITAL SYSTEM DESIGN	36	0	0
17231D6101	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	33	14	0
17231D6101	I6803	CMOS ANALOG IC DESIGN	33	46	1
17231D6101	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	30	1
17231D6102	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	31	-1	0
17231D6102	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	-1	0
17231D6102	I5704	FRONT END VLSI DESIGN LABORATORY	34	51	1
17231D6102	I6801	DIGITAL SYSTEM DESIGN	33	7	0
17231D6102	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	29	-1	0
17231D6102	I6803	CMOS ANALOG IC DESIGN	29	-1	0
17231D6102	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	29	14	0
17231D6103	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	32	35	1
17231D6103	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	31	40	1
17231D6103	I5704	FRONT END VLSI DESIGN LABORATORY	36	53	1
17231D6103	I6801	DIGITAL SYSTEM DESIGN	34	10	0
17231D6103	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	32	15	0
17231D6103	I6803	CMOS ANALOG IC DESIGN	35	43	1
17231D6103	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	31	29	1
17231D6104	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	37	26	1
17231D6104	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	32	38	1
17231D6104	I5704	FRONT END VLSI DESIGN LABORATORY	36	54	1
17231D6104	I6801	DIGITAL SYSTEM DESIGN	37	24	1
17231D6104	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	33	18	0
17231D6104	I6803	CMOS ANALOG IC DESIGN	36	44	1
17231D6104	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	38	27	1
17231D6105	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	31	4	0
17231D6105	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	33	29	1
17231D6105	I5704	FRONT END VLSI DESIGN LABORATORY	33	52	1
17231D6105	I6801	DIGITAL SYSTEM DESIGN	32	6	0
17231D6105	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	32	17	0
17231D6105	I6803	CMOS ANALOG IC DESIGN	33	48	1
17231D6105	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	31	28	1
17231D6106	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	19	-1	0
17231D6106	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	17	-1	0
17231D6106	I5704	FRONT END VLSI DESIGN LABORATORY	0	-1	0
17231D6106	I6801	DIGITAL SYSTEM DESIGN	18	-1	0
17231D6106	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	15	-1	0
17231D6106	I6803	CMOS ANALOG IC DESIGN	17	-1	0
17231D6106	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	19	-1	0
17231D6107	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	34	32	1
17231D6107	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	31	41	1
17231D6107	I5704	FRONT END VLSI DESIGN LABORATORY	34	53	1
17231D6107	I6801	DIGITAL SYSTEM DESIGN	36	24	1
17231D6107	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	33	30	1
17231D6107	I6803	CMOS ANALOG IC DESIGN	31	25	1
17231D6107	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	28	1

Htno	Subcode	Subname	Internal	External	credits
17231D6108	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	35	7	0
17231D6108	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	32	31	1
17231D6108	I5704	FRONT END VLSI DESIGN LABORATORY	35	53	1
17231D6108	I6801	DIGITAL SYSTEM DESIGN	36	0	0
17231D6108	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	34	19	0
17231D6108	I6803	CMOS ANALOG IC DESIGN	32	36	1
17231D6108	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	28	1
17231D6109	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	36	4	0
17231D6109	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	34	1
17231D6109	I5704	FRONT END VLSI DESIGN LABORATORY	34	54	1
17231D6109	I6801	DIGITAL SYSTEM DESIGN	36	12	0
17231D6109	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	33	25	1
17231D6109	I6803	CMOS ANALOG IC DESIGN	35	34	1
17231D6109	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	35	31	1
17231D6110	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	35	33	1
17231D6110	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	31	44	1
17231D6110	I5704	FRONT END VLSI DESIGN LABORATORY	33	53	1
17231D6110	I6801	DIGITAL SYSTEM DESIGN	32	29	1
17231D6110	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	34	40	1
17231D6110	I6803	CMOS ANALOG IC DESIGN	31	47	1
17231D6110	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	36	37	1
17231D6111	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	38	30	1
17231D6111	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	43	1
17231D6111	I5704	FRONT END VLSI DESIGN LABORATORY	39	56	1
17231D6111	I6801	DIGITAL SYSTEM DESIGN	38	8	0
17231D6111	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	38	1
17231D6111	I6803	CMOS ANALOG IC DESIGN	37	42	1
17231D6111	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	38	17	0
17231D6112	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	36	26	1
17231D6112	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	29	41	1
17231D6112	I5704	FRONT END VLSI DESIGN LABORATORY	30	50	1
17231D6112	I6801	DIGITAL SYSTEM DESIGN	30	25	1
17231D6112	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	32	33	1
17231D6112	I6803	CMOS ANALOG IC DESIGN	33	47	1
17231D6112	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	35	31	1
17231D6113	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	14	-1	0
17231D6113	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	17	-1	0
17231D6113	I5704	FRONT END VLSI DESIGN LABORATORY	0	-1	0
17231D6113	I6801	DIGITAL SYSTEM DESIGN	14	-1	0
17231D6113	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	14	-1	0
17231D6113	I6803	CMOS ANALOG IC DESIGN	16	-1	0
17231D6113	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	16	-1	0
17231D6114	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	35	7	0
17231D6114	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	33	33	1
17231D6114	I5704	FRONT END VLSI DESIGN LABORATORY	37	55	1
17231D6114	I6801	DIGITAL SYSTEM DESIGN	37	12	0
17231D6114	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	35	13	0
17231D6114	I6803	CMOS ANALOG IC DESIGN	31	51	1
17231D6114	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	27	1

**Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through Online(www.jntukresults.edu.in))

NOTE:2 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **09-05-2018]

**NOTE:3 [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]

**NOTE:
[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject.]

N. Mohan Rao

Date:26-04-2018

Controller of Examinations